IAM



## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q76993

Sadayuki OHNISHI

Appln. No.: 10/646,709

Group Art Unit: 2814

Confirmation No.: 9821

Examiner: Phat X. CAO

Filed: August 26, 2003

For:

SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

## RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated August 6, 2004 in which the Examiner has required a restriction, Applicant responds by electing Group I, *i.e.*, claims 1-13, drawn to an apparatus. This election is made without prejudice to the filing of a divisional application directed to the non-elected claims.

Prompt and favorable action on the elected claims is now respectfully requested.

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Date: September 3, 2004

Respectfully submitted,

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